

Date	Rev.	Notice	Create by
2020/10/26	V090	LC0815	WS
2020/11/04	V100	1. [Page5] Modify OPT1 SPDIF out circuit and change connector to JST2B15 2. [Page6] Modify Q4 PMOS gate pin, from XGPIO_1 to XGPIO_14. 3. [Page3] Remove R33/R36/D4 4. [Page3] Modify XGPIO_1 ~ XGPIO_3 as I2S_DAC4 5. Add R68~78 0ohm Resistor 6. [Page3] Add J10~J13 jumper 7. [Page3] USB1 change to Micro B type 8. [Page3] J8 change to 4x2 header,add X12M_CLK options 9. [Page3] I2C&Reset pull up voltage change to DVDD3.3V	Marconi

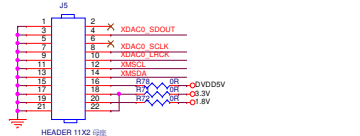
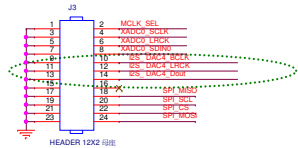
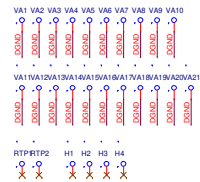
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CM6212_ TDM_DEMOBOARD_ V100

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The diagram illustrates the connection of the SPI interface to the Xilinx Spartan-3E Starter Kit. It shows four connections:

- SPI_MISO** connected to **XD2** via connector **J10**.
- SPI_SCL** connected to **XD0** via connector **J11**.
- SPI_CS** connected to **XD3** via connector **J12**.
- SPI_MOSI** connected to **XD1** via connector **J13**.

Each connection is represented by a red line with a blue box indicating the connector pin header. The pins are labeled J10, J11, J12, and J13 respectively.

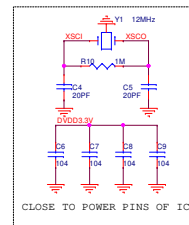
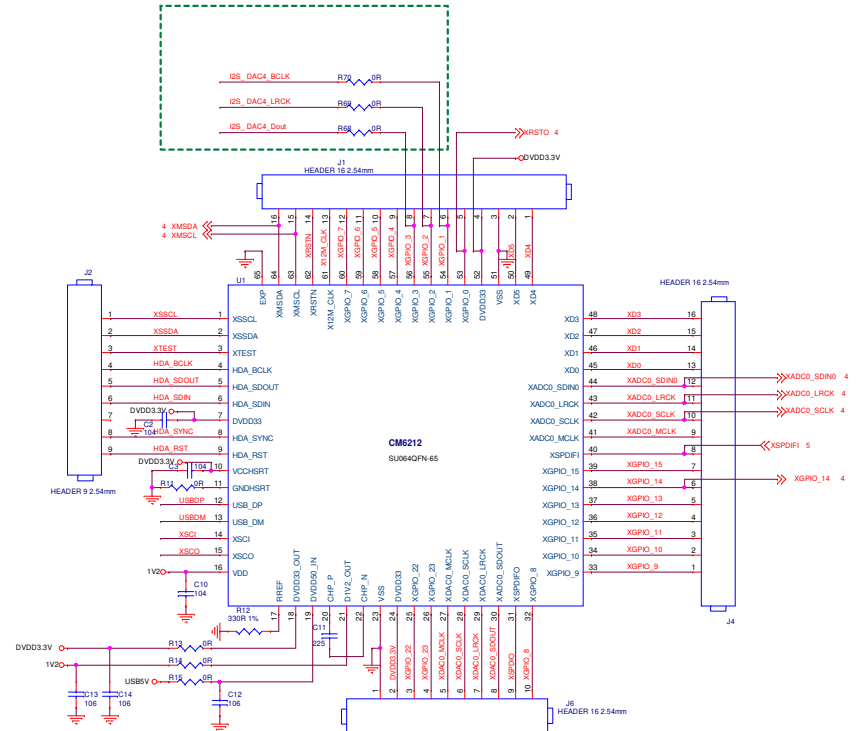
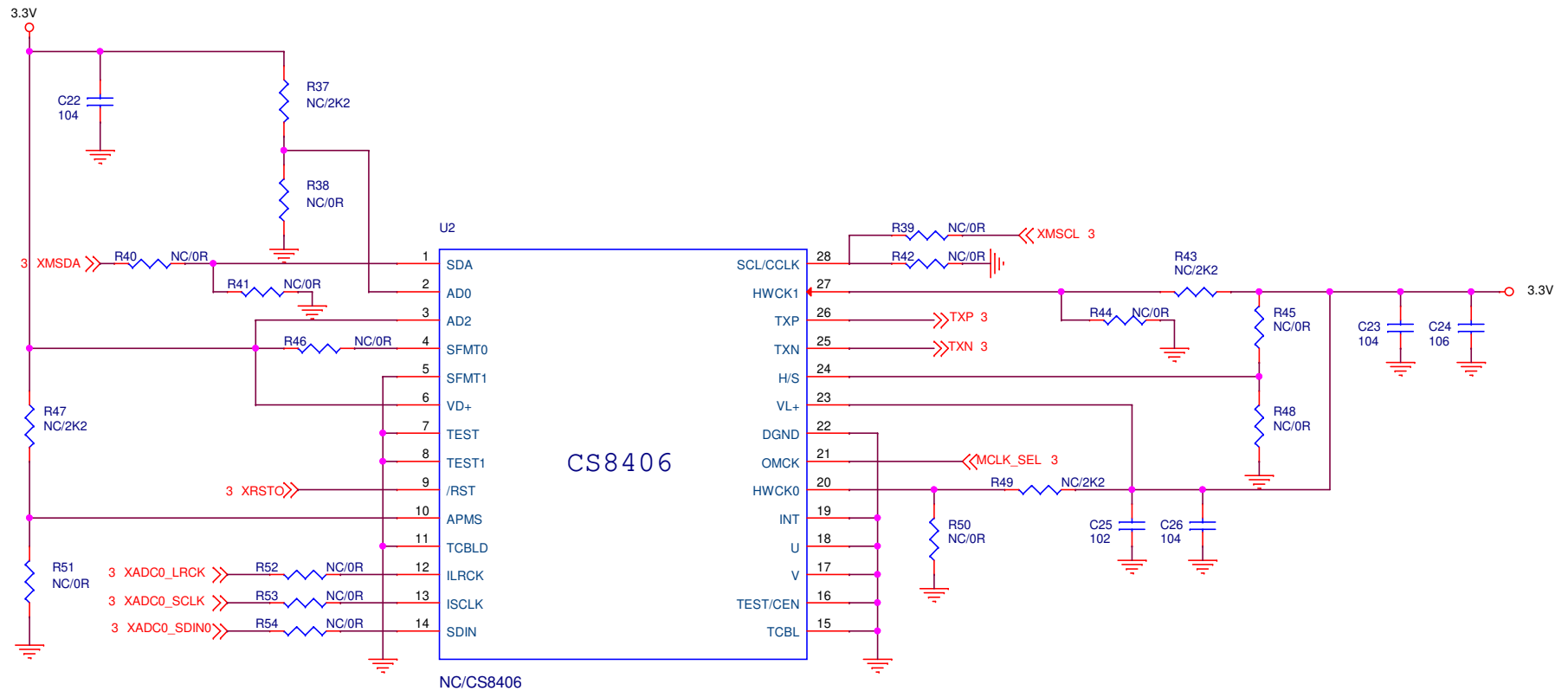


Diagram of the 4X2 pin header for the Xilinx XC4005. The header has 8 pins. Pin 1 is labeled 'XDAC0 MCLK'. Pin 2 is labeled 'TRAC0 MCLK'. Pin 3 is labeled 'XDAC0 MCLK'. Pin 4 is labeled 'TRAC0 MCLK'. Pin 5 is labeled 'XDAC0 MCLK'. Pin 6 is labeled 'TRAC0 MCLK'. Pin 7 is labeled 'XDAC0 MCLK'. Pin 8 is labeled 'TRAC0 MCLK'. A signal line labeled 'MCLK_SEL 4' is connected to pin 4.

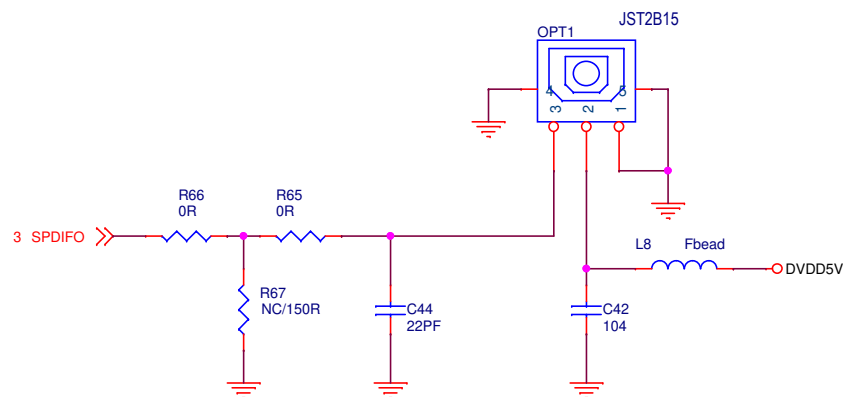




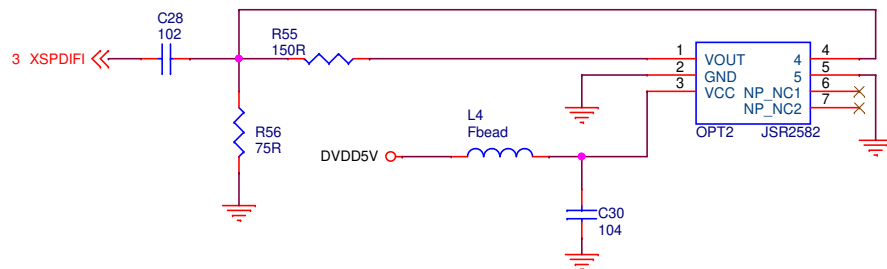
Pin 10 set LOW (Slave mode)
 Pin 4 set HIGH, Pin 5 set LOW (I2S mode)
 Pin 20 set LOW, Pin 27 set LOW 256Fs)

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SPDIF-OUT



SPDIF In



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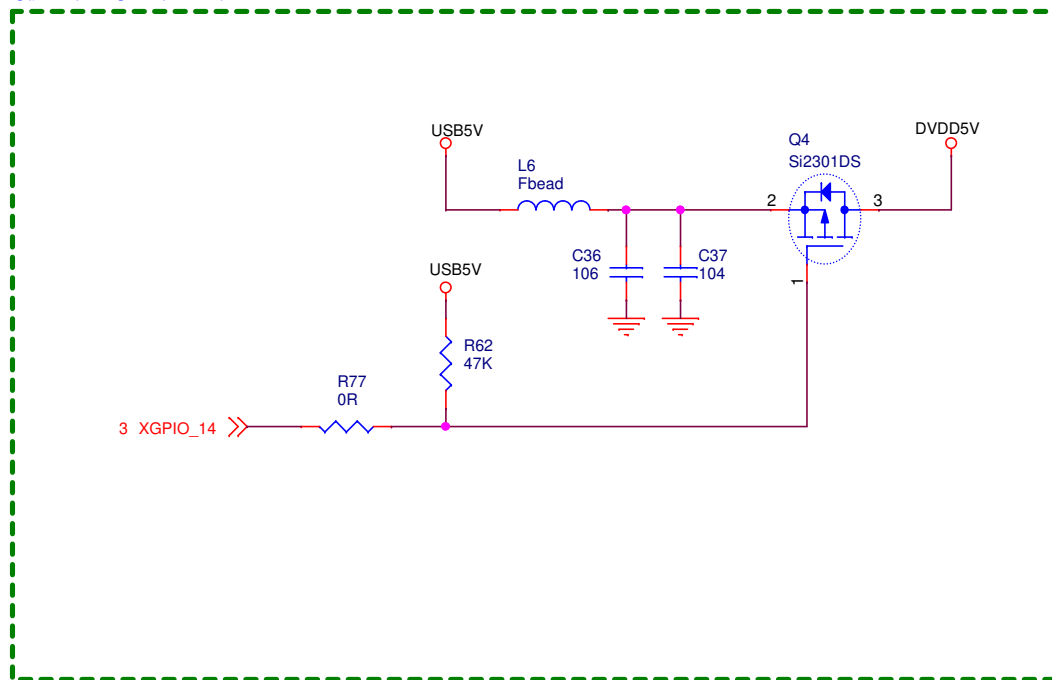
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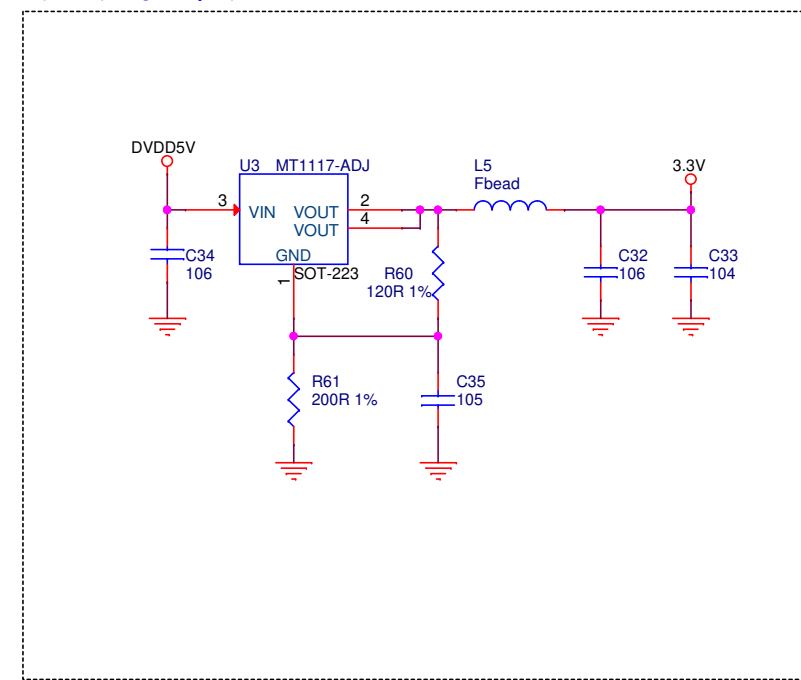
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USB5V to DVDD5V



DVDD5V to 3.3V



3.3V to 1.8V

